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** CONTINUING DATA ***** *affirmed (SR)*

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** FOREIGN APPLICATIONS ***** *affirmed (SR)*

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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged <i>Satish Ramphal</i> Examiner's Signature Initials	JAPAN	40	8	2

ADDRESS

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TITLE

Processor for executing instructions in units that are unrelated to the units in which instructions are read, and a compiler, an optimization apparatus, an assembler, a linker, a debugger and a disassembler for such processor

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